



33773M064

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Toshiharu Daii, et al.

U.S. Serial No.: 10/774,529

Group Art Unit: To Be Assigned

Filed: : February 10, 2004

Examiner: To Be Assigned

For: SEMICONDUCTOR WAFER PROCESSING METHOD

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. 1.56, Applicants are enclosing an Information Disclosure Citation Form (PTO-1449) and a copy of the cited document. The document was cited and discussed on page 2 of the Applicants' specification.

It is respectfully requested that the cited document be considered by the Examiner in the above-identified patent application and that the cited document be made officially of record therein. It is further requested that a listing of the same appear on the face of any patent which may issue from this application.

This Information Disclosure Statement is being filed within three (3) months of the filing date of the application. Therefore, it is believed that no fees are due under 37 C.F.R. Section 1.97(b)(3).

Respectfully submitted,
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March 2, 2004

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET

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U.S. PATENT DOCUMENTS

*Examiner's Initials		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE, IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

*Examiner's Initials		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
							YES	NO
	AH	2001-257248	9/21/01	Japan				x
	AI							
	AJ							
	AK							

OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

	AL	
	AM	
	AN	
	AO	
	AP	
	AQ	
	AR	

EXAMINER:

DATE CONSIDERED:

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.